

ABSTRACT OF THE DISCLOSURE

In a digital camera, an image data of an object is taken by an image pickup device, the image data is processed predetermined data processes such as white balance adjustment, data compression and extension, γ compensation, and contour emphasizing, and the image data is communicated between the digital camera and another electronic equipment such as a personal computer. Data processors and communication interface are respectively configured by a common FPGA (Field Programmable Gate Array) which can serve as plural kinds of dedicated circuits by witting different programs. In image pickup mode, a compression program is automatically written in the FPGA for serving as a JPEG data compressor. Alternatively, in reproducing mode, an extension program is written in the FPGA for serving as a JPEG data extender. When an RS-232C cable is connected to a connector of the digital camera, an RS-232C program is automatically written in the FPGA for serving as an RS-232C interface. Alternatively when a USB cable is connected, a USB program is automatically written in the FPGA for serving as a USB interface.